

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10055722	FILING DATE 01/23/2002	CLASS 257	SUBCLASS 257	GAU 2811
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EXAMINER
Estelle Lee

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- Estelle Lee

**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

JAPAN JP 2001-015042 01/23/2001

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>
Foreign priority claimed 35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> yes <input type="checkbox"/> no
Verified and Acknowledged Examiner's initials		
TITLE : Semiconductor integrated circuit device and manufacture method therefore U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-91)		

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
Amount Due	Date Paid		Total Claims	Print Claim for O.G.
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	DRAWING	
PREPARED FOR ISSUE			Sheets Drwg.	Figa.Drwg.
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